

REMARKS

Claims 1-21 are pending in this application. In response to the Examiner's Restriction Requirement set forth in the August 25, 2004 Office Action, Applicants provisionally elect for prosecution with traverse, Group II, including claims 8-17 and 20-21, drawn to a method for fabricating a semiconductor device.

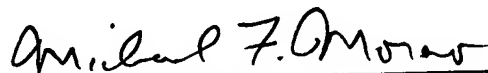
As set forth in M.P.E.P. § 803, if the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits, even though the application contains claims to independent or distinct inventions.

While the semiconductor device and method for fabricating a semiconductor device of Groups I and II may be distinct for the reasons set forth by the Examiner, it is respectfully submitted that simultaneous examination will not present an undue burden, much less any burden.

For example, the claims similarly include recitations to conductive patterns of a semiconductor device and a method for fabricating a semiconductor device including conductive patterns. Under such circumstances, the Examiner is encouraged to maintain all claims in the same application (See M.P.E.P. § 803).

An early and favorable consideration of this application is earnestly solicited.

Respectfully submitted,



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